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U.S. **UTILITY** Patent Application

<div style="display: flex; justify-content: space-between;"><div><div style="text-align: center;">M.H. SCANNED</div><div style="text-align: center;">O.I.P.E. UR</div><div style="text-align: center;">Q.A. TC</div></div><div style="text-align: right;">PATENT DATE</div></div>

APPLICATION NO. 09/637214	CONT/PRIOR F	CLASS 740 75	SUBCLASS 302	ART UNIT 2182	EXAMINER [Signature]
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APPLICANTS

Heinz Baier

TITLE

Method and system for programming FPGAs on PC-cards without additional hardware

PTO-2040
12/89

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
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